

UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Naoki YADA et al.

Appln. No.: 10/080,681

Group Art Unit: 2188

Filed: February 25, 2002

Examiner: Thang H. HO

For: MICROCOMPUTER, PROGRAMMING METHOD AND ERASING METHOD

REQUEST FOR APPROVAL OF DRAWING CORRECTIONS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 RECEIVED

JUL 0 7 2004

Sir:

Technology Center 2100

Attached hereto are copies of Figs. 10 and 21 of the present application, which have been marked in red ink. The Examiner is kindly requested to review and approve the drawing corrections and provide Applicants' undersigned representative with written notice of such approval in the next Patent Office paper.

Respectfully submitted,

MWS: JHV: adc

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